UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/695,422	10/28/2003	Vinodh Ravindran	112-0138US	4486

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EXAMINER
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ART UNIT	PAPER NUMBER
2109	

MAIL DATE	DELIVERY MODE
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)
Office Action Summary		10/695,422	RAVINDRAN ET AL.
		Examiner	Art Unit	
		Anish Sikri	2109	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on <u>28 October 2003</u>.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-12* is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-12* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>28 October 2003</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
    Paper No(s)/Mail Date _____.

4) ☐ Interview Summary (PTO-413)
    Paper No(s)/Mail Date. _____.

5) ☐ Notice of Informal Patent Application

6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a

foreign country or in public use or on sale in this country, more than one year

prior to the date of application for patent in the United States.

Claims **1 to 12** are rejected under 35 U.S.C. 102(b) as being anticipated by

Padovano (US Pat 6,606,690 B2).

Consider **Claim 1,** Padovano clearly discloses storage processing device

(Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66) comprising: an

input/output module including: port processors to receive and transmit network traffic

(Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66, Col 2, Lines 15-65),

and a switch coupling said port processors (Padovano, Fig 11, Col 4, Line 14-16, Col 7,

Lines 33-45) and a control module coupled to said input/output module (Padovano, Fig

4, Col 16, Lines 25-60, Col 17 Lines 3-12), said input/output module and said control

module being configured to interactively support data mirroring (Padovano, Fig 4, Col

16, Lines 25-60, Col 17 Lines 3-12). It clearly shows on how a SAN

appliance/server/device with and administrative interface is configured to a switched

network to provide data mirror capabilities.

Consider **Claim 2**, Padovano clearly discloses storage processing device of

claim 1, wherein said port processors include table information relating to data mirroring

and wherein said control module is coupled to said table information to maintain said

table information for data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines

3-12). It clearly shows on how the device provides data management functionality when

conducting data mirroring processes.

Consider **Claim 3**, Padovano clearly discloses the storage processing device of

claim 1, wherein said port processors perform the data and command replication and

response gathering operations to support data mirroring (Padovano, Fig 4, Col 16, Lines

25-60, Col 17 Lines 19-52). It clearly shows that the storage-processing device has an

administrative interface, which aids in response gathering operations of the data

mirroring processes.

Consider **Claim 4**, Padovano clearly discloses a fabric for coupling at least one

host and at least two storage devices (Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66,

Col 8 Lines 11-39) the fabric comprising: at least one switch for coupling to the at least

one host and the at least two storage devices (Padovano, Fig 8, Col 7 Lines 24-32,

Lines 61-66, Col 8 Lines 11-39); and a storage processing device coupled to the at least

one switch and for coupling to the at least one host and the at least two storage devices

Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39) the storage

processing device (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66)

including: an input/output module including: port processors to receive and transmit

network traffic (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66, Col 2,

Lines 15-65); and a switch coupling said port processors (Padovano, Fig 11, Col 4, Line

14-16, Col 7, Lines 33-45); and a control module coupled to said input/output module

(Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12), said input/output module and

said control module being configured to interactively support data mirroring (Padovano,

Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It clearly shows on how a SAN

appliance/server/device/fabric with and administrative interface is configured to a

switched network to provide data mirror capabilities.

Consider **Claim 5**, Padovano clearly discloses a fabric of claim 4, wherein said

port processors include table information relating to data mirroring and wherein said

control module is coupled to said table information to maintain said table information for

data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It clearly

shows on how the fabric provides data management functionality when conducting data

mirroring processes.

Consider **Claim 6**, Padovano clearly discloses the fabric of claim 4, wherein said

port processors perform the data and command replication and response gathering

operations to support data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17

Lines 19-52). It clearly shows that the fabric has an administrative interface, which aids

in response gathering operations of the data mirroring processes.

Consider **Claim 7**, Padovano clearly discloses a network comprising (Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39): at least one host; at least two storage devices (Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39); and a fabric for coupling at least one host and at least two storage devices (Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39) the fabric comprising: at least one switch for coupling to the at least one host and the at least two storage devices (Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39); and a storage processing device coupled to the at least one switch and for coupling to the at least one host and the at least two storage devices Padovano, Fig 8, Col 7 Lines 24-32, Lines 61-66, Col 8 Lines 11-39) the storage processing device (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66) including: an input/output module including: port processors to receive and transmit network traffic (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1, Lines 55-66, Col 2, Lines 15-65); and a switch coupling said port processors (Padovano, Fig 11, Col 4, Line 14-16, Col 7, Lines 33-45); and a control module coupled to said input/output module (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12), said input/output module and said control module being configured to interactively support data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It clearly shows on how a SAN appliance/server/device/fabric with and administrative interface is configured to a switched network to provide data mirror capabilities.

Consider **Claim 8**, Padovano clearly discloses a network of claim 7, wherein said port processors include table information relating to data mirroring and wherein said

control module is coupled to said table information to maintain said table information for

data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It clearly

shows on how the devices on the network provide data management functionality when

conducting data mirroring processes.

Consider **Claim 9**, Padovano clearly discloses a network of claim 7, wherein said

port processors perform the data and command replication and response gathering

operations to support data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17

Lines 19-52). It clearly shows on how the SAN devices on the network have an

administrative interface, which aids in response gathering operations of the data

mirroring processes.

Consider **Claim 10**, Padovano clearly discloses a method for supporting data

mirroring in a storage processing device (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7,

Col 1, Lines 55-66) comprising: an input/output module including: port processors to

receive and transmit network traffic (Padovano, Fig 1, Fig 3A, Fig 4, Fig 5, Fig 7, Col 1,

Lines 55-66, Col 2, Lines 15-65), and a switch coupling said port processors

(Padovano, Fig 11, Col 4, Line 14-16, Col 7, Lines 33-45) and a control module coupled

to said input/output module (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12),

said input/output module and said control module being configured to interactively

support data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It

clearly shows on how a SAN appliance/server/device with and administrative interface

is configured to a switched network to provide data mirror capabilities.

Consider **Claim 11**, Padovano clearly discloses a method of claim 10, wherein said port processors include table information relating to data mirroring and wherein said control module is coupled to said table information to maintain said table information for data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 3-12). It clearly shows on how the device provides data management functionality when conducting data mirroring processes.

Consider **Claim 12**, Padovano clearly discloses the method of claim 10, wherein said port processors perform the data and command replication and response gathering operations to support data mirroring (Padovano, Fig 4, Col 16, Lines 25-60, Col 17 Lines 19-52). It clearly shows that the storage-processing device has an administrative interface, which aids in response gathering operations of the data mirroring processes.

Conclusion

Any response to this Office Action should be **faxed to** (571) 273-8300 **or mailed to**:

> Commissioner for Patents
> P.O. Box 1450
> Alexandria, VA 22313-1450

Hand-delivered responses should be brought to

> Customer Service Window
> Randolph Building
> 401 Dulany Street
> Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Anish Sikri whose telephone number is (571) 270-1783. The Examiner can normally be reached on Monday-Thursday from 6:30am to 5:00pm.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Rafael Pérez-Gutiérrez can be reached on (571) 272-7915. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you

have questions on access to the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free) or 571-272-4100.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the receptionist/customer service whose telephone

number is (571) 272-2600.

Anish Sikri
A.S./as

May 21, 2007

RAFAEL PEREZ-GUTIERREZ
SUPERVISORY PATENT EXAMINER

5/22/07